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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

MAY 02 2006

In re Application of: SUNG, Raymond, Jit-Hung; ELLIOTT, Duncan, George
Serial No.: 10/730,002
Filed: December 9, 2003
Title: CLOCK LOGIC DOMINO CIRCUITS FOR HIGH-SPEED AND
ENERGY EFFICIENT MICROPROCESSOR PIPELINES
Group: 2183
Examiner: CHO, James Hyonchol
Attorney Ref.: PAT 3454-2 US

May 2, 2006

Commissioner for Patents
United States Patent and Trademark Office
Randolph Building
401 Dulany Street
Alexandria, Virginia
22314
U.S.A.

Mail Stop **AMENDMENT**

Dear Sir:

AMENDMENT

In response to the Office Action dated March 2, 2006, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 4 of this paper.

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of 5/17/06